

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

TR. KSTD Q.A. CH

APPLICATION NO. 09/740634	CONT/PRIOR D F	CLASS 324	SUBCLASS 763	ART UNIT 3829 2858	EXAMINER L. P. Patel
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APPLICANTS

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Device for measurement and analysis of electrical signals of an integrated circuit component

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	 _____ (Primary Examiner) (Date)		ISSUE FEE	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		Amount Due	Date Paid
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